

J1033 U.S. PTO
09/19/67
09/27/01

| ISSUE CLASSIFICATION | |
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| Class | Subclass |
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PATENT NUMBER

U.S. UTILITY Patent Application

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|------------------------------------|-------------|
| O.I.P.E. | PATENT DATE |
| SCANNED <i>HP</i> <i>HP</i> 3 Q.A. | |

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| APPLICATION NO. 09/966721 | CONT/PRIOR F | CLASS 438 | SUBCLASS 106 + | ART UNIT -2812- | EXAMINER Zhuo |
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APPLICANTS

Takashi Hosaka

3. Results

Structure of a semiconductor module and method of manufacturing the same

PTO-2040
12/89[illegible]

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|---|--|-------------|------------|-----------------------------------|----------------------|
| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |
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